

Mapping Very Large Scale Spiking Neuron Network to Neuromorphic Hardware

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Outline

- Background
- Method
- Experiments
- Conclusion



Background

Neuromorphic Computing



Imitating the brain in terms of neuron and synaptic connection models, **Spiking Neural Network** (SNN) features rich spatialtemporal information and high biological plausibility.



Background Neuromorphic Hardware

A neuromorphic hardware platform is a computer system specifically designed to implement SNN applications. They using a large number of specially

designed neurosynaptic cores to simulate neurons dynamics in parallel.





Background

Spiking Neuron Network (SNN) mapping problem



Background Neuromorphic Hardware

Table 1: Capacity of several neuromorphic hardware platforms

Multiple new neuromorphic computing platforms, including Loihi 2, SpiNNaker 2 and Darwin 3, aim to reach **tens of billions** of neuron capacities.

| | DYNAPs [24] | BrainScaleS [30] | Loihi [7] | SpiNNaker [11] | TrueNorth [8] | | | |
|----------------------------|----------------|---------------------|--------------|-------------------|------------------|--|--|--|
| # Neurons/core | 256 | 512 | 128 | 1000 | 256 | | | |
| <pre># Synapses/core</pre> | 16K | 128K | 500K | 2K | 262K | | | |
| # cores/chip | 1 | 1 | 1024 | 18 | 4096 | | | |
| <pre># chips/system</pre> | 4 | 8192 | 768 | 1M | 64 | | | |
| High-performance system | | | | | | | | |
| # Neurons | 1K | 4M | 100M | 1B | 64M | | | |
| # Synapses | 65K | 1B | 100B | 200B | 1T | | | |

Background

related work

- Greedy Algorithm
- Heuristic search algorithms: Particle Swarm Optimization, Simulated Annealing, Genetic Algorithm
- Integer Linear Programming (ILP)

Motivation

Existing algorithms lack scalability and are unable to efficiently map large scale SNNs to neuromorphic Hardware

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Contribution

- We are the first to apply Hilbert Space-filing Curve (HSC) to the SNN mapping problem.
- We propose the Force Directed (FD) algorithm
- We evaluate our approach with a large scale of 4 billion neurons and millions of cores on a general neuromorphic hardware model.

Method Hilbert Space-filling Curve (HSC)

The Hilbert curve is a continuous fractal space-filling curve first described by the German mathematician David Hilbert. It provides a mapping relationship between 1D and 2D space.



David Hilbert



Properties of HSC

- Infinity
- Provide dataflow layout
- Locality

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Figure 5: Data flow layout

Locality

• Locality of HSC

Two data points which are close to each other in 1D space are also close to each other after folding.

• Locality of SNN

Neurons are only connected to a few other neurons locally instead of being widely connected in the whole network.



The HSC provides a placement that only maps clusters at a macro level, so there is a large room for local optimization. Therefore, we propose the FD algorithm to finetune the placement provided by HSC.

Force Directed (FD) algorithm

The main idea of the FD algorithm is to regard clusters as particles on a 2D plane and the connection between clusters as the tension



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System modelling

Potential energy field:

$$U_{c_i}(c_j, P(c_i), P(c_j)) = u(P(c_j) - P(c_i)) * w_p(e_{i,j}).$$
(18)

Cluster energy:

$$E_{c_i} = \sum_{c_j \mid e_{j,i} \in E_p} U_{c_j}(c_i, P(c_j), P(c_i))$$
(22)

System energy:

$$E_{s} = \sum_{c_{i} \in V_{P}} E_{c_{i}}$$

= $\sum_{c_{i} \in V_{P}} \sum_{c_{j} | e_{j,i} \in E_{p}} U_{c_{j}}(c_{i}, P(c_{j}), P(c_{i}))$ (23)
= $\sum_{e_{i,j} \in E_{p}} U_{c_{i}}(c_{j}, P(c_{i}), P(c_{j})).$

Target:

 $\underset{P}{\operatorname{argmin}} E_s.$



Algorithm modelling

Force:

Force<sub>c_i,d
=
$$E_{c_i} - E'_{c_i}$$

= $E_{c_i} - \sum_{c_j \mid e_{j,i} \in E_p} U_{c_j}(c_i, P(c_j), P(c_i) + \nabla p_d),$ (27)</sub>

where

$$\nabla p_{d} = \begin{cases} (UP, DOWN, LEFT, RIGHT) \\ (-1, 0) \quad d = UP \\ (1, 0) \quad d = DOWN \\ (0, -1) \quad d = LEFT \\ (0, 1) \quad d = RIGHT \end{cases}$$
(28)

Tension :

$$Tension_{c_i,c_j} = Force_{c_i,d_{ij}} + Force_{c_j,d_{ji}}$$

where $||P(c_i) - P(c_j)|| = 1,$ (30)

Work flow

- 1. Compute tensions of all pairs
- 2. Sort pairs according to the tensions
- 3. Swap pairs in the front of the queue
- 4. Repeat until convergence



Design Choices of FD Algorithm

- Check before the swapping process The convergence of the algorithm is guaranteed
- Hyperparameter λ Balance the efficiency of the algorithm with the quality of the solution
- Introducing of L_{affected}
 Significantly reduce the algorithm overhead when approaching convergence

Experiments

Setting

Comparison Approaches

- The baseline: Randomly mapping
- Truenorth
- DFSynthesizer
- PSO
- Proposed approach

Evaluation Metrics

- Energy consumption
- Average latency
- Maximum latency
- Average congestion
- Maximum congestion
- Algorithm execution time

Table 3: Benchmarks

| | G _{SNN} | | G_{PCN} | | Target |
|----------------|------------------|----------|-----------|-------------|--------------------|
| Applications | Neurons | Synapses | Clusters | Connections | Hardware |
| DNN_65K | 65536 | 805M | 16 | 48 | 4 × 4 |
| DNN_16M | 16.7M | 4T | 4096 | 258048 | 64×64 |
| DNN_268M | 268M | 70T | 65536 | 4M | 256×256 |
| DNN_4B | 4B | 1125T | 1M | 67M | 1024×1024 |
| CNN_65K | 65536 | 2M | 16 | 48 | 4×4 |
| CNN_16M | 16.7M | 528M | 4096 | 16384 | 64×64 |
| CNN_268M | 268M | 8B | 65536 | 262K | 256×256 |
| | | | | | |
| LeNet-MNIST | 9118 | 0.4M | 9 | 19 | 3×3 |
| LeNet-ImageNet | 1.0M | 188M | 251 | 2151 | 16×16 |
| AlexNet | 0.9M | 1.0B | 229 | 4289 | 16×16 |
| MobileNet | 6.9M | 0.5B | 1688 | 37418 | 42×42 |
| InceptionV3 | 14.6M | 5.4B | 3570 | 117597 | 60×60 |
| ResNet | 28.5M | 11.6B | 6956 | 478602 | 84 × 84 |

Table 2: Parameters of target neuromorphic hardware

| Parameter | Value | |
|-------------|-------|--|
| CONnpc | 4096 | |
| CON_{spc} | 64K | |
| EN_r | 1 | |
| EN_w | 0.1 | |
| L_r | 1 | |
| L_{w} | 0.01 | |
| | | |

Experiments

Algorithm execution time



Figure 9: Results on execution time



Experiments

Results on energy consumption



Figure 10: Results on energy consumption

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Conclusion

We proposed an approach based on HSC and FD to map very large scale SNN applications to neuromorphic hardware, achieved the SOTA performance in both solving speed and quality of the solution.



